

REMARKS/ARGUMENTS

In the Office action dated January 19, 2007, claims 1 – 7, 11 – 15, 18, and 19 are allowed and claims 21 – 23 are rejected. In response, Applicant has amended claim 21. Applicant respectfully requests further examination of the claims in view of the amendment and the remarks provided herein.

Allowable Subject Matter

Applicant notes with appreciation that claims 1 – 7, 11 – 15, 18, and 19 are allowed.

Claim 21

The Office action points out that claim 21 as previously presented did not contain limitations similar to the limitation argued in independent claims 1, 12, and 18. Specifically, claim 21 did not include “wherein the bit sequences are ‘stored in a register array’.” Applicant has amended claim 21 to include the limitation “*storing a plurality of bit sequences in a register array*” and to modify the “generating” step to recite “*the plurality of bit sequences that are stored in the register array*.” No new matter has been added and the new limitations are supported at least by paragraphs [0034] – [0036] and Fig. 4 of Applicant’s specification.

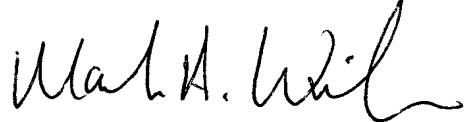
Applicant asserts that claim 21 as amended includes limitations which are similar to limitations in claims 1, 12, and 18 and which are not taught or suggested by the prior art. A notice of allowance is respectfully requested.

Applicant asserts that claims 22 and 23 are allowable at least based on an allowable claim 21.

CONCLUSION

Applicant respectfully requests reconsideration of the claims in view of the remarks made herein. A notice of allowance is earnestly solicited.

Respectfully submitted,



Date: April 17, 2007

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